

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	("6233599").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 15:23
S1	42	((("6069514") or ("6111859") or ("6222409") or ("6252264") or ("6309915") or ("6429536") or ("6437618") or ("6452931") or ("6456407") or ("6463497") or ("6463033") or ("4672607") or ("4760571") or ("4933938") or ("5229993") or ("5341369") or ("5485578") or ("5594728") or ("6049824") or ("6311212") or ("6339788"))).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/14 11:26
S2	357	(task adj switching) same (register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/14 13:25
S3	319	(718/108).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/14 11:32
S4	14	S2 and S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/14 11:32
S5	868	((process or context) adj switching) same (register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/14 13:25
S6	319	(718/108).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/14 13:25

S7	64	((process or context) adj switching) same (register) and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/14 18:33
S8	1075	(718/102).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/14 18:33
S9	319	(718/108).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/14 18:34
S10	64	((process or context) adj switching) same (register) and S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/14 18:41
S11	18	((process or context) adj switching) same (register) and S8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/14 18:40
S12	5016	("718").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/14 18:41
S14	152	((process or task or context) adj switching) same (register) and S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/14 18:46
S15	326	((process or task or context) adj (identif\$)) same loading or switching) same (register) and S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/14 18:48

S16	14	((process or task or context) adj (identif\$)) same loading or switching) same (first adj register) and S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/14 18:51
S17	40	((process or task or context) adj (identif\$)) same loading or switching) same ((plurality or multiple or first) adj register) and S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/14 19:03
S18	1	Tagged and Progammer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/18 17:09
S19	868	((process or context) adj switching) same (register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/18 17:11
S20	355	(blocked or hidden or programmer) and S19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/18 17:12
S21	319	(718/108).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/18 17:11
S22	64	((process or context) adj switching) same (register) and S21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/18 17:11
S23	31	(blocked or hidden or programmer) and S22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/18 17:15

S24	5016	("718").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/18 17:11
S25	326	((process or task or context) adj (identif\$)) same loading or switching) same (register) and S24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/18 17:11
S26	158	(blocked or hidden or programmer) and S25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/18 17:11
S27	277	(programmer) and S19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/18 17:15
S28	23	(programmer) and S22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/18 17:15
S29	104	(programmer) and S25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/18 17:22
S30	160	(programmer) same (hidden or blocked or exposed) same (register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/07/18 17:23

**Key:** IEEE JNL = IEEE Journal or Magazine, IEE JNL = IEE Journal or Magazine, IEEE CNF = IEEE Conference, II CNF = IEE Conference, IEEE STD = IEEE Standard

1. **SCALP: an iterative-improvement-based low-power data path synthesis system**  
Raghunathan, A.; Jha, N.K.;  
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on  
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2. **Micro-preemption synthesis: an enabling mechanism for multi-task VLSI systems**  
Kim, K.; Karri, R.; Potkonjak, M.;  
Computer-Aided Design, 1997. Digest of Technical Papers., 1997 IEEE/ACM International Conference on  
9-13 Nov. 1997 Page(s):33 - 38  
IEEE CNF
3. **An evaluation to the use of the non-overlapped multiple register sets in the network nodes processor**  
Elkateeb, A.; Saleem, D.;  
Communications, Computers and Signal Processing, 1997. '10 Years PACRIM 1987-1997 - Networking the Pacific Rim'. 1997 IEEE Pacific Rim Conference on  
Volume 2, 20-22 Aug. 1997 Page(s):887 - 890 vol.2  
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4. **Flexible register window structure for multi-tasking**  
Jung, G.; Meyer, D.G.; Milutinovic, V.;  
System Sciences, 1991. Proceedings of the Twenty-Fourth Annual Hawaii International Conference on  
Volume 1, 8-11 Jan. 1991 Page(s):110 - 116 vol.1  
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5. **A fast switching double processing architecture for multi-tasking real-time systems**  
Tein-Hsiang Lin; Jui-ping Liao;  
Parallel and Distributed Systems, 1994. International Conference on  
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6. **Service control capabilities for intelligent mobile communications network**  
Nakamura, H.; Kimura, K.; Nakajima, A.;  
Networks for Personal Communications, 1994. Conference Proceedings. 1994  
16-18 March 1994 Page(s):1 - 5  
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7. **Extension of functional possibilities of mm-range modulation radiometers**  
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Physics and Engineering of Millimeter and Submillimeter Waves, 1998. MSMW '98. Third International Kharkov Symposium  
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8. **Register window management for a real-time multitasking RISC**  
Quammen, D.J.; Miller, D.R.; Tabak, D.;  
System Sciences, 1989. Vol.1: Architecture Track, Proceedings of the Twenty-Second Annual Hawaii International Conference on  
Volume 1, 3-6 Jan. 1989 Page(s):135 - 142 vol.1  
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9. **Ultrafast all-optical switches in fiber networks**  
Islam, M.N.; Sauer, J.R.;  
Optical Multiple Access Networks, 1990. Conference Digest. LEOS Summer Topical on  
25-27 July 1990 Page(s):37 - 38

**10. A mechanism for efficient context switching**

Nuth, P.R.; Dally, W.J.;

Computer Design: VLSI in Computers and Processors, 1991. ICCD '91. Proceedings., 1991 IEEE International Conference on

14-16 Oct. 1991 Page(s):301 - 304

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**11. Real-time operating system performance issues and techniques with RISC**

Ball, T.;

RISC Architectures and Applications, IEE Colloquium on

4 Nov 1991 Page(s):7/1 - 7/8

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**12. Assignment-space exploration approach to testable data-path synthesis for minimizing partial scan register**

Kaneko, M.; Shimizu, Y.; Tayu, S.;

Circuits and Systems, 2000. IEEE APCCAS 2000. The 2000 IEEE Asia-Pacific Conference on

4-6 Dec. 2000 Page(s):540 - 543

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**13. A three dimensional register file for superscalar processors**

Tremblay, M.; Joy, B.; Shin, K.;

System Sciences, 1995. Proceedings of the Twenty-Eighth Hawaii International Conference on

Volume 1, 3-6 Jan. 1995 Page(s):191 - 201 vol.1

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**14. Interconnection minimization in multiport memories based data path synthesis**

Ahmad, I.; Chen, C.Y.R.;

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